

FIG. 1

EXHIBIT

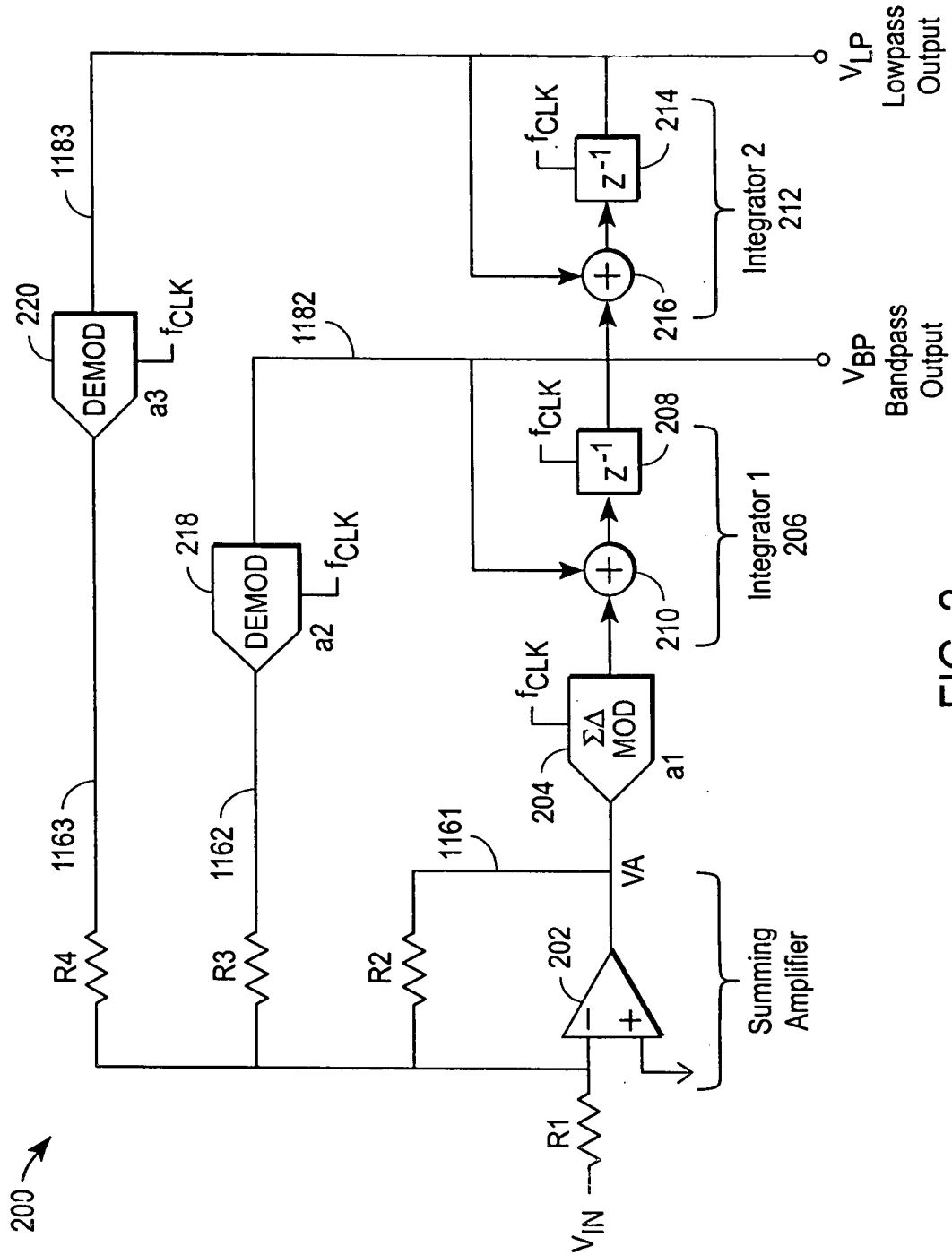


FIG. 2

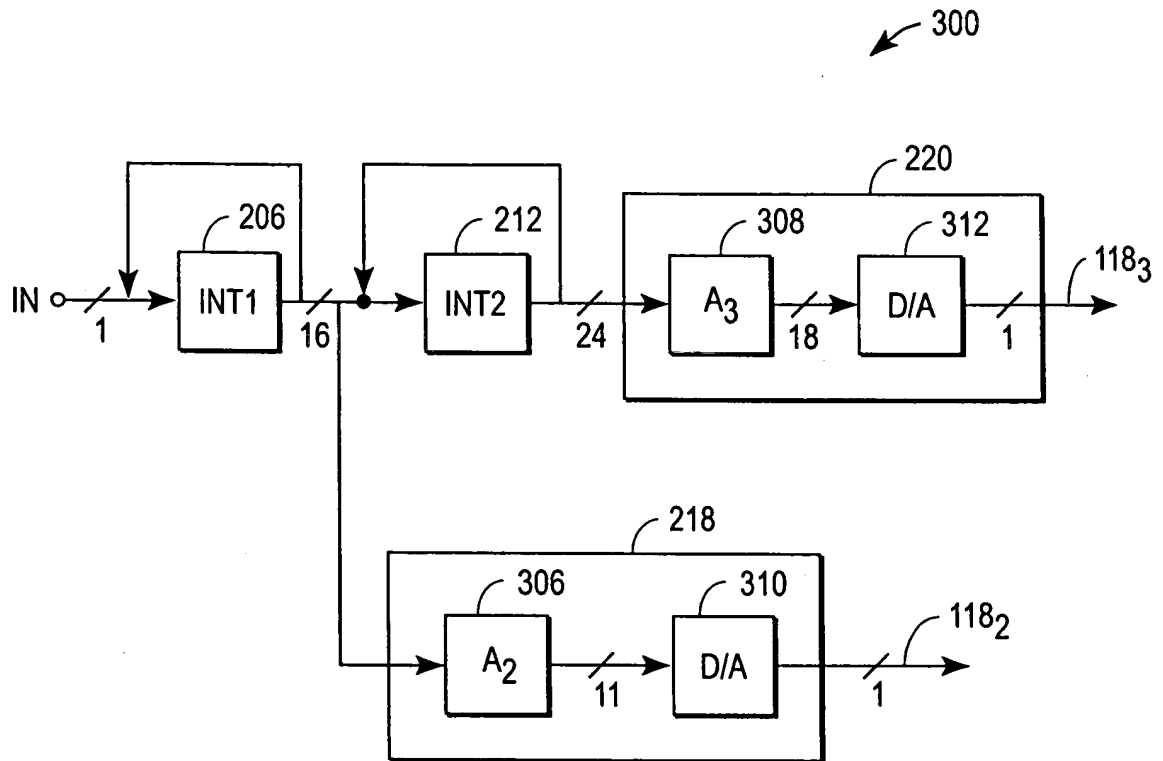
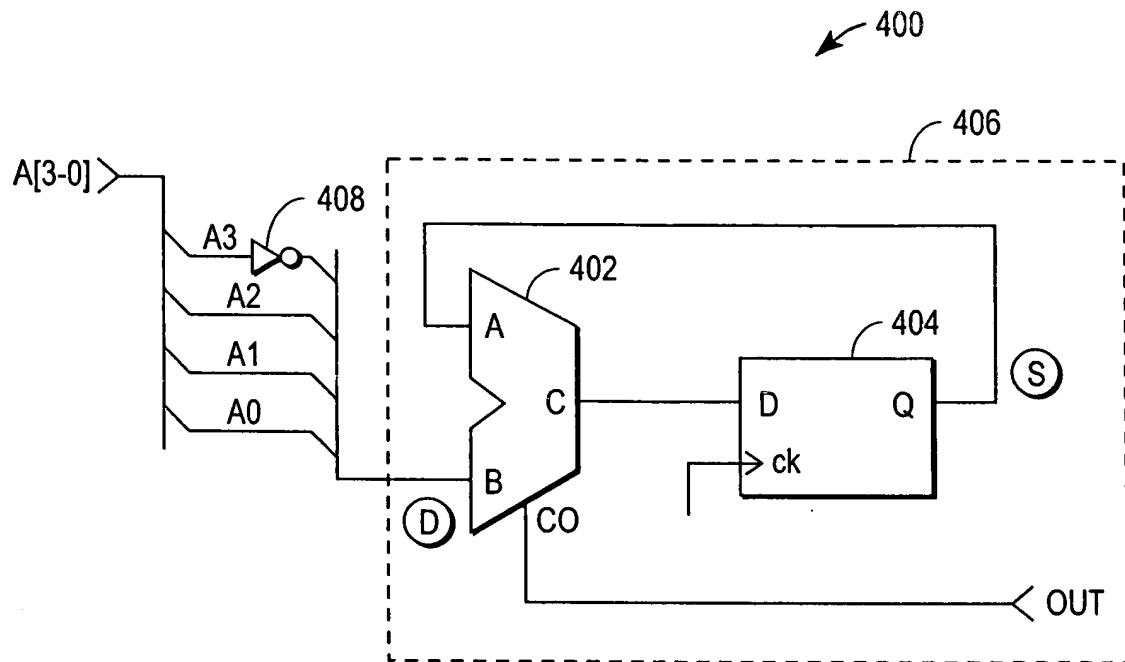


FIG. 3



Input: Two's Complement  
Output: One Bit  
HI = + 5V  
LO = - 5V

FIG. 4

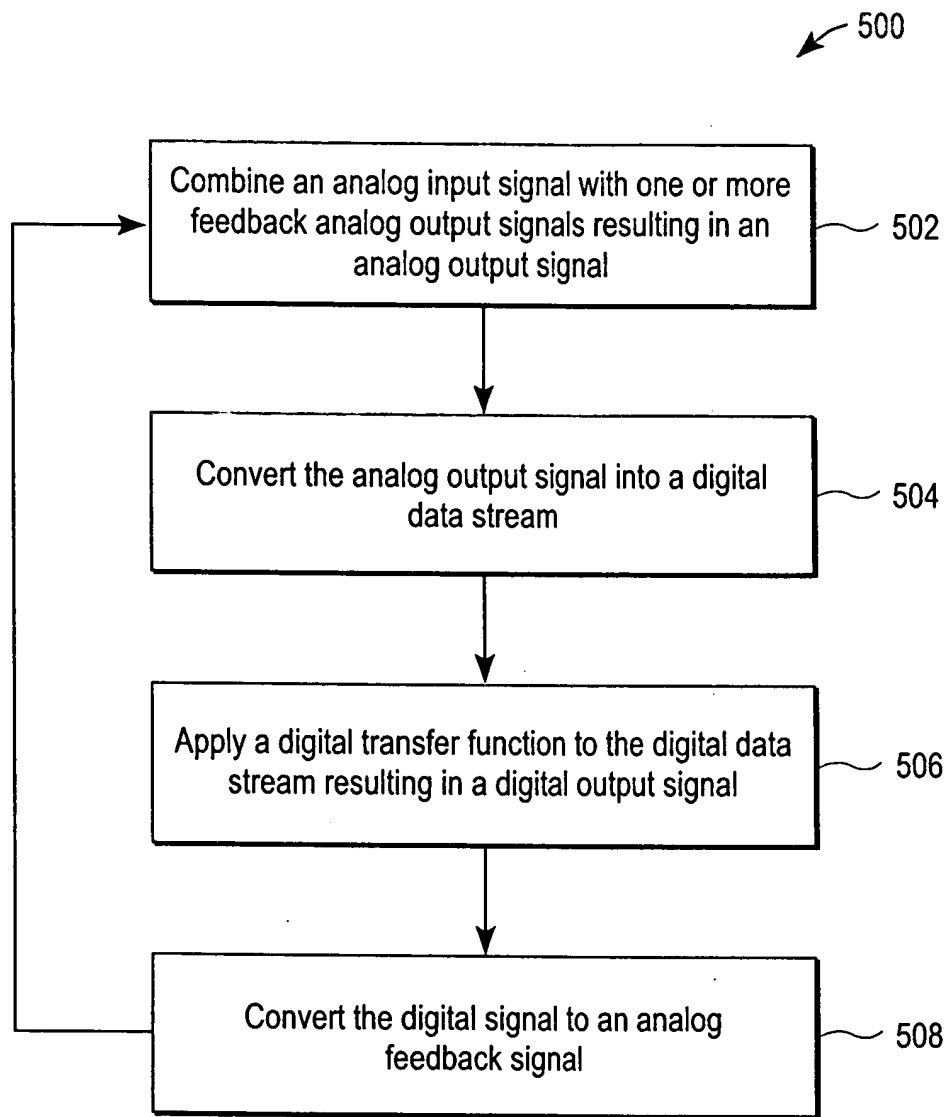


FIG. 5